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U.S. Patent No. 7,088,682

First Named Inventor: Balaji Lakshmikanth Bangolae

Issue Date: August 8, 2006

Title: Reducing Overhead When Using Loopback Cells for Fault Detection in Bi-Directional Virtual Circuits

Art Unit: 2616

Confirmation No.: 3956

Examiner Name: Christine Y. Ng

Attorney Docket Number: CSCO-009/119928

I hereby revoke all previous powers of attorney given in the above-identified application.

I hereby appoint Practitioner(s) associated with the following Customer Number as my/our attorney(s) or agent(s) to prosecute the application identified above and to transact all business in the United States Patent and Trademark Office connect therewith:

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I am the Assignee of record of the entire interest.

/Mallun Yen/
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April 1, 2009

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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Cisco Technology, Inc.

Patent No. 7,088,682

Issue Date:

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Entitled: Reducing Overhead When Using Loopback Cells for Fault Detection in Bi-Directional Virtual Circuits

Cisco Technology, Inc. Assignee, a California corporation, states that it is the assignee of the entire right, title, and interest, by virtue of an Assignment recorded on August 9, 2001, in the United States Patent and Trademark Office at Reel 012067, Frame 0887.

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

/Mallun Yen/
Signature of Applicant or Assignee of RecordApril 1, 2009Mallun Yen
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